

PLUS search history

10752504_CLSTITLES.txt

Titles of Most Frequently Occurring Classifications of Patents Returned
From A Search of 10752504 on June 16, 2006

4 365/233 (1 OR, 3 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/230.01 ADDRESSING
365/233 .Sync/clocking

4 716/6 (3 OR, 1 XR)
Class 716 : DATA PROCESSING: DESIGN AND ANALYSIS OF
CIRCUIT OR SEMICONDUCTOR MASK
716/1 CIRCUIT DESIGN
716/4 .Testing or evaluating
716/5 ..Design verification (e.g., wiring line
capacitance, fan-out checking, minimum path width)
716/6 ...Timing analysis (e.g., delay time, path
delay, latch timing)

3 327/296 (0 OR, 3 XR)
Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
DEVICES, CIRCUITS, AND SYSTEMS
327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
327/291 .Clock or pulse waveform generating
327/295 ..Plural outputs
327/296 ...Plural clock outputs with multiple inputs

3 327/299 (0 OR, 3 XR)
Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
DEVICES, CIRCUITS, AND SYSTEMS
327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
327/291 .Clock or pulse waveform generating
327/299 ..Single clock output with single clock input
or data input

3 341/143 (2 OR, 1 XR)
Class 341 : CODED DATA GENERATION OR CONVERSION
341/126 ANALOG TO OR FROM DIGITAL CONVERSION
341/143 .Differential encoder and/or decoder (e.g.,
delta modulation, differential pulse code modulation)

3 370/516 (0 OR, 3 XR)
Class 370 : MULTIPLEX COMMUNICATIONS
370/473 ..Transmission of a single message having
multiple packets
370/498 .Combining or distributing information via time
channels
370/503 ..Synchronizing
370/516 ...Adjusting for phase or jitter

3 711/167 (3 OR, 0 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/167 .Access timing

2 327/149 (0 OR, 2 XR)
Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
DEVICES, CIRCUITS, AND SYSTEMS
327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
327/141 .Synchronizing
327/144 ..Using multiple clocks
327/146 ...With feedback

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327/147Phase lock Loop
327/149with variable delay means

2 327/156 (2 OR, 0 XR)
Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
DEVICES, CIRCUITS, AND SYSTEMS
327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
327/141 .Synchronizing
327/155 ..With feedback
327/156 ...Phase lock loop

2 327/158 (0 OR, 2 XR)
Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
DEVICES, CIRCUITS, AND SYSTEMS
327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
327/141 .Synchronizing
327/155 ..With feedback
327/156 ...Phase lock loop
327/158With variable delay means

2 327/163 (2 OR, 0 XR)
Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
DEVICES, CIRCUITS, AND SYSTEMS
327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
327/141 .Synchronizing
327/162 ..Having reference source
327/163 ...By phase

2 327/234 (0 OR, 2 XR)
Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
DEVICES, CIRCUITS, AND SYSTEMS
327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
327/231 .Phase shift by less than period of input
327/233 ..Correction to specific phase shift
327/234 ...Dependent on variable controlled phase
shifts

2 327/239 (0 OR, 2 XR)
Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
DEVICES, CIRCUITS, AND SYSTEMS
327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
327/231 .Phase shift by less than period of input
327/237 ..Variable or adjustable
327/239 ...Non-overlapping multiple outputs

2 365/227 (0 OR, 2 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/226 POWERING
365/227 .Conservation of power

2 370/503 (1 OR, 1 XR)
Class 370 : MULTIPLEX COMMUNICATIONS
370/473 ..Transmission of a single message having
multiple packets
370/498 .Combining or distributing information via time
channels
370/503 ..Synchronizing

2 375/247 (1 OR, 1 XR)
Class 375 : PULSE OR DIGITAL COMMUNICATIONS
375/242 PULSE CODE MODULATION
375/244 .Differential
375/247 ..Single bit (delta)

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2 711/100 (1 OR, 1 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL

2 713/503 (0 OR, 2 XR)
Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: SUPPORT
713/500 CLOCK, PULSE, OR TIMING SIGNAL GENERATION OR
ANALYSIS
713/503 .Correction for skew, phase, or rate

2 714/799 (1 OR, 1 XR)
Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY
714/699 PULSE OR DATA ERROR HANDLING
714/799 .Error/fault detection technique